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APPLICANTS Masahiro Ishida, Tokyo, JAPAN; Takahiro Yamaguchi, Tokyo, JAPAN; Yoshihiro Hashimoto, Tokyo, JAPAN;						
** CONTINUING DATA ***** This application is a DIV of 09/980,891 12/03/2001 PAT 6,801,049 which is a 371 of PCT/JP01/02910 04/04/2001 <i>ok OAL</i>						
** FOREIGN APPLICATIONS ***** JAPAN 2000-101867 04/04/2000 <i>ok OAL</i>						
** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 05/11/2004						
Foreign Priority claimed <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No 35 USC 119(a-d) conditions met <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No Verified and Acknowledged <u>/OSCAR A LOUIE/</u> Examiner's Signature		<input type="checkbox"/> Met after Allowance OAL Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWINGS 38	TOTAL CLAIMS 3	INDEPENDENT CLAIMS 3
ADDRESS OSHA LIANG L.L.P. 1221 MCKINNEY STREET SUITE 2800 HOUSTON, TX 77010 UNITED STATES						
TITLE Method and apparatus for defect analysis of semiconductor integrated circuit						
FILING FEE RECEIVED 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:			<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		